

FIG. 1

PEARCE 26; 09/755,826  
METHOD OF MANUFACTURING A LATERALLY  
DIFFUSED METAL OXIDE SEMICONDUCTOR DEVICE  
Hitt Gaines, PC; Greg H. Parker (972)480-8800

REPLACEMENT  
SHEET

2/6

FIG. 2

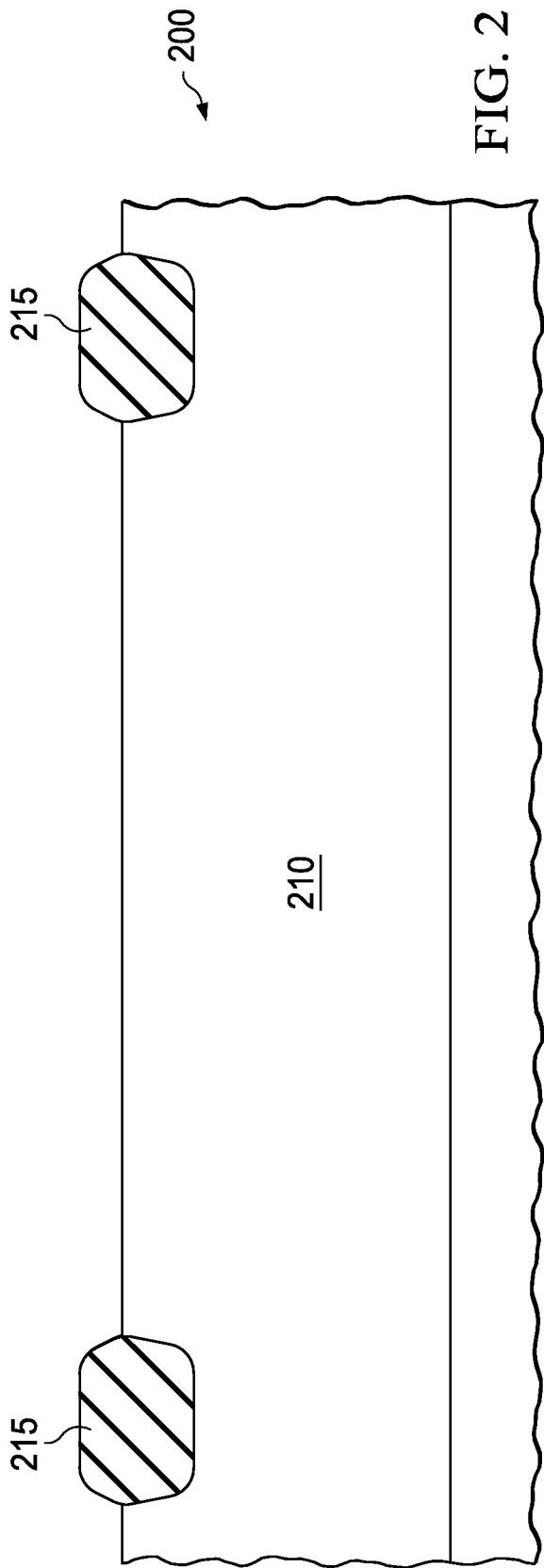
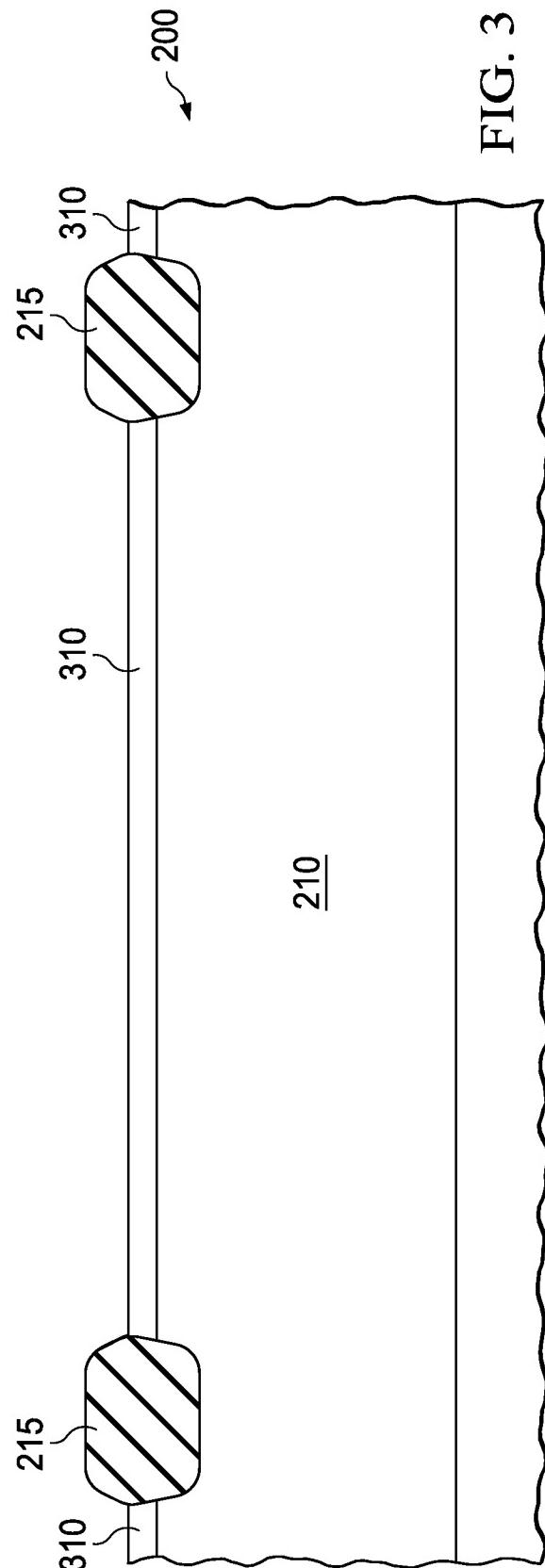


FIG. 3



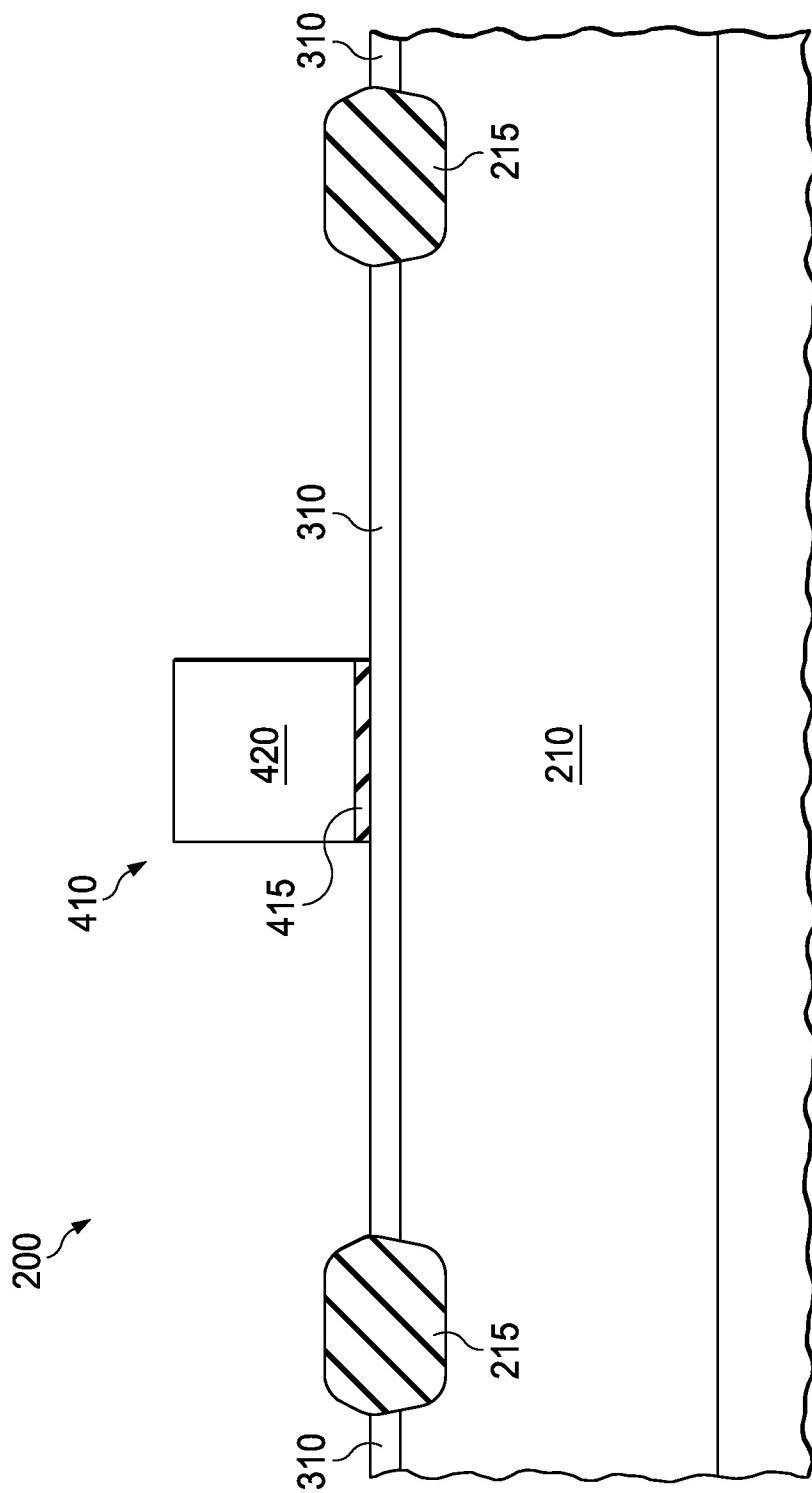


FIG. 4

PEARCE 26; 09/755,826  
METHOD OF MANUFACTURING A LATERALLY  
DIFFUSED METAL OXIDE SEMICONDUCTOR DEVICE  
Hitt Gaines, PC; Greg H. Parker (972)480-8800

REPLACEMENT  
SHEET

4/6

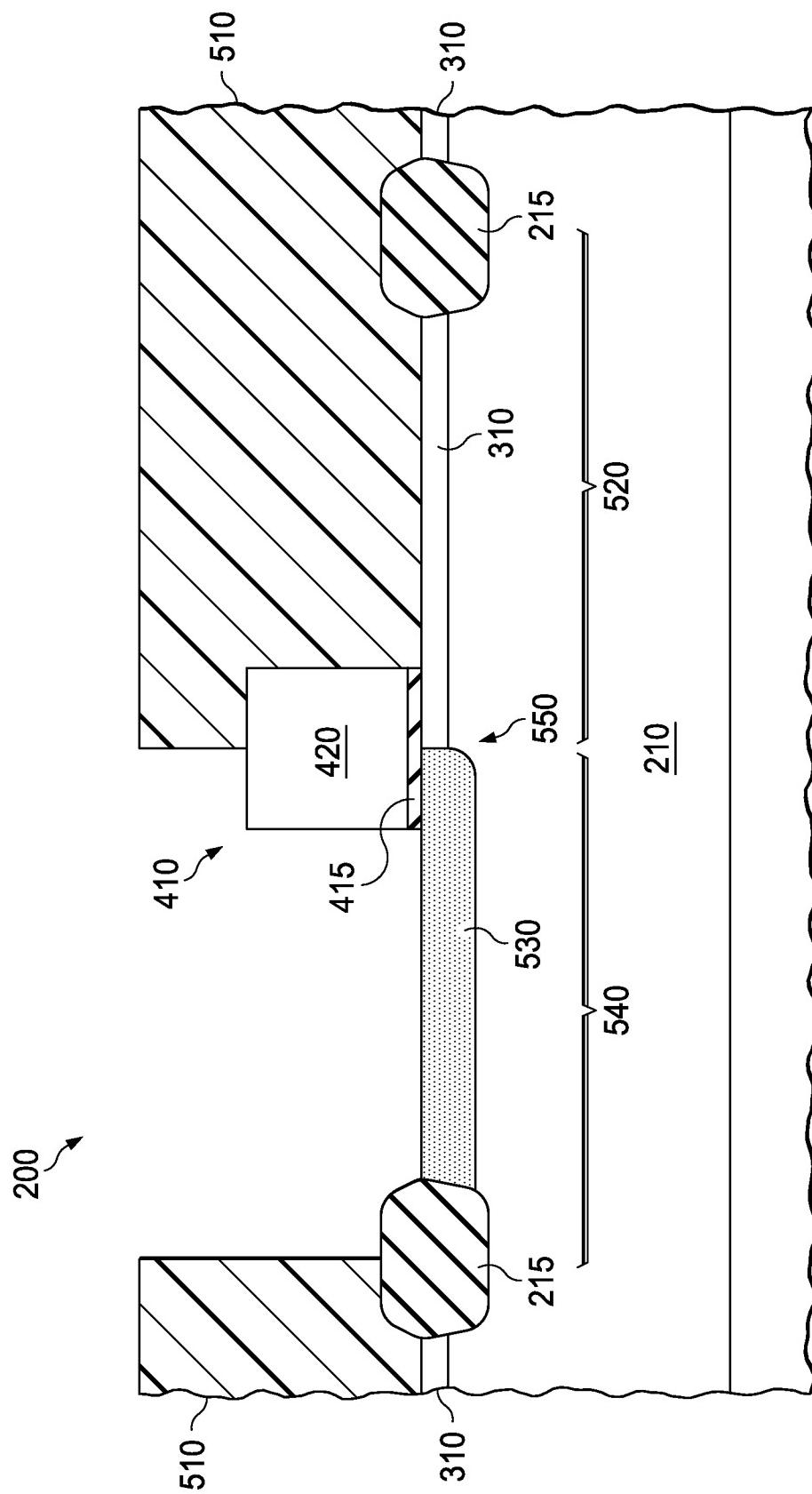


FIG. 5

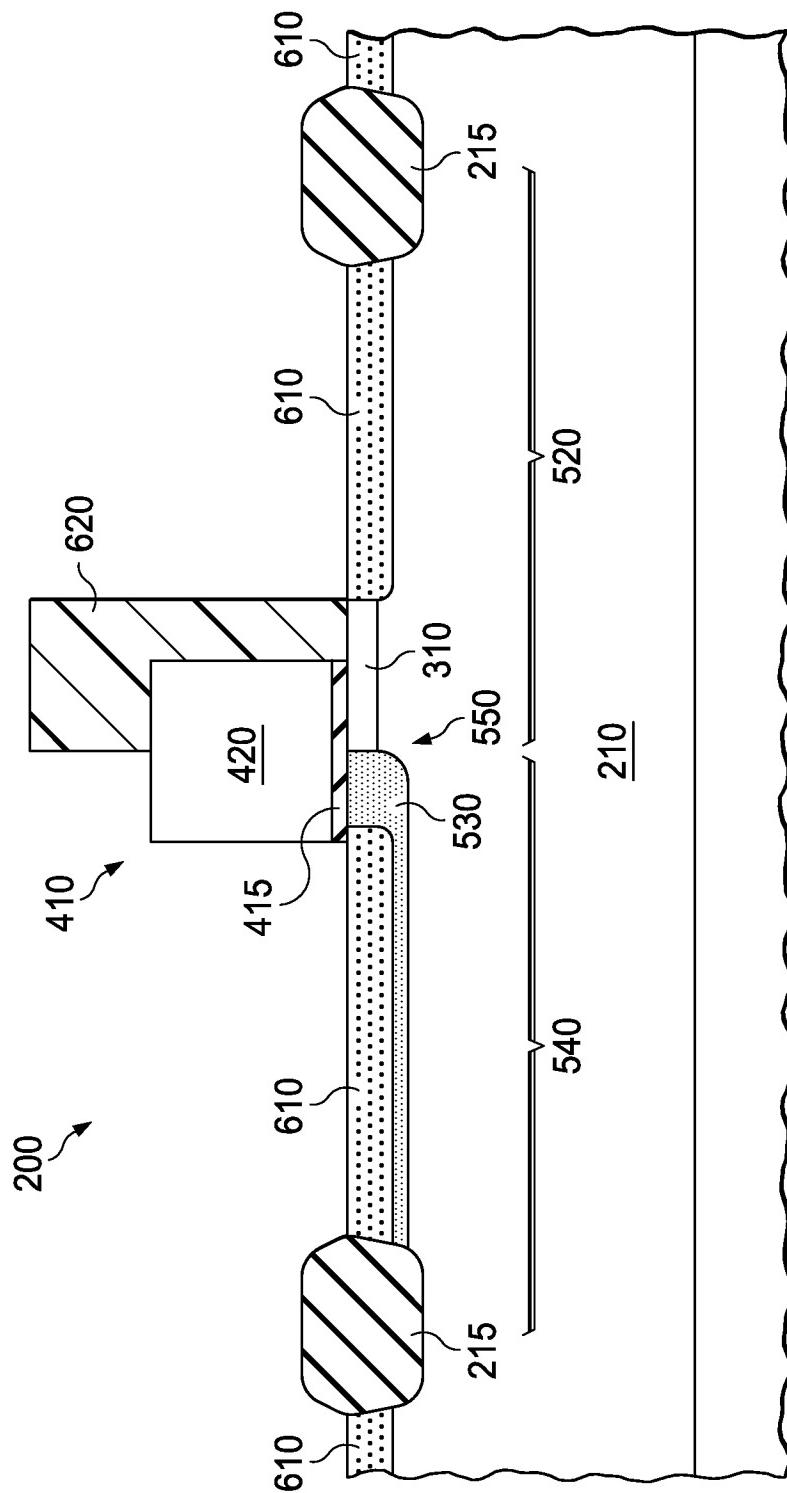


FIG. 6

PEARCE 26; 09/755,826  
METHOD OF MANUFACTURING A LATERALLY  
DIFFUSED METAL OXIDE SEMICONDUCTOR DEVICE  
Hitt Gaines, PC; Greg H. Parker (972)480-8800

REPLACEMENT  
SHEET

6/6

FIG. 7

